

FIG. 1

10040291-10501
FIG. 1

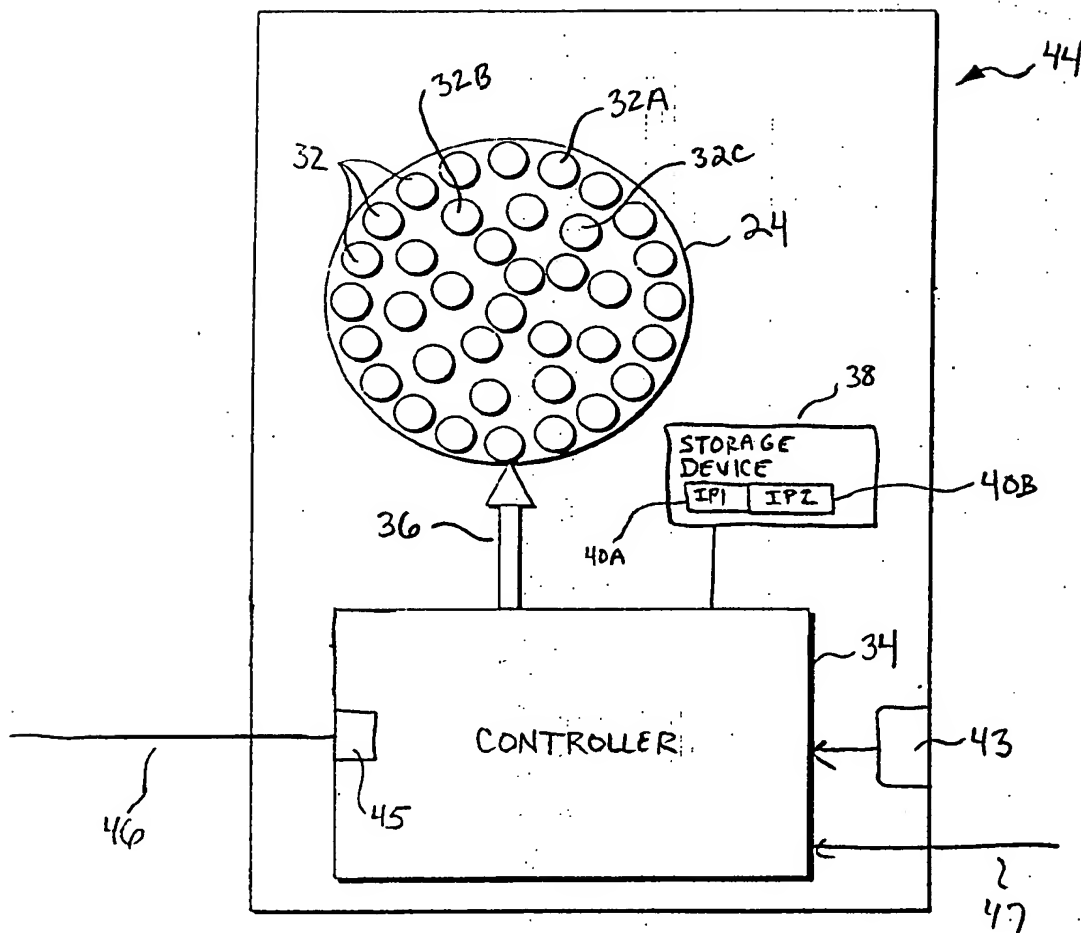


Fig. 2

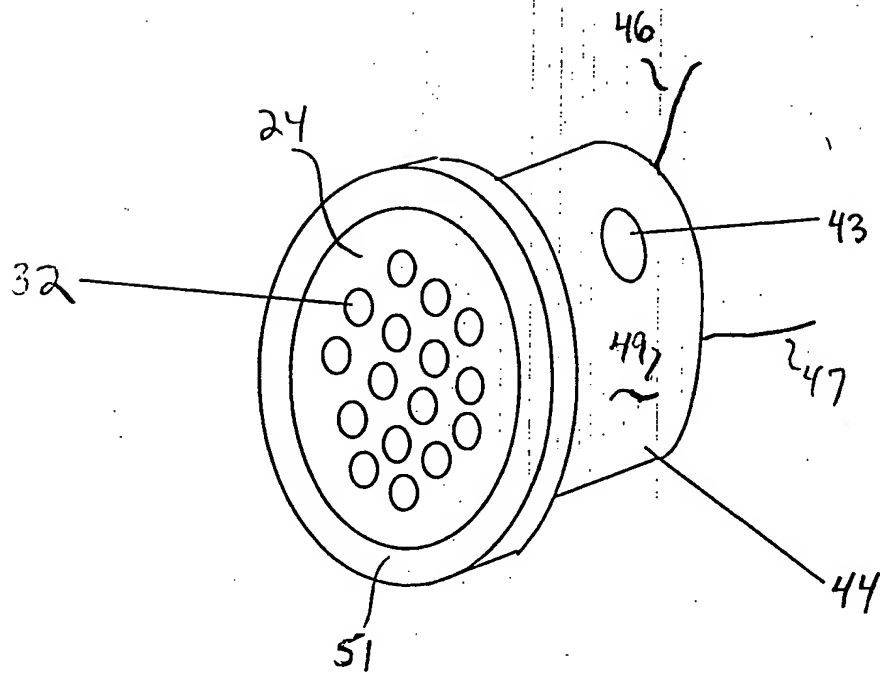


FIG. 3

10040291-10501

FIG. 4

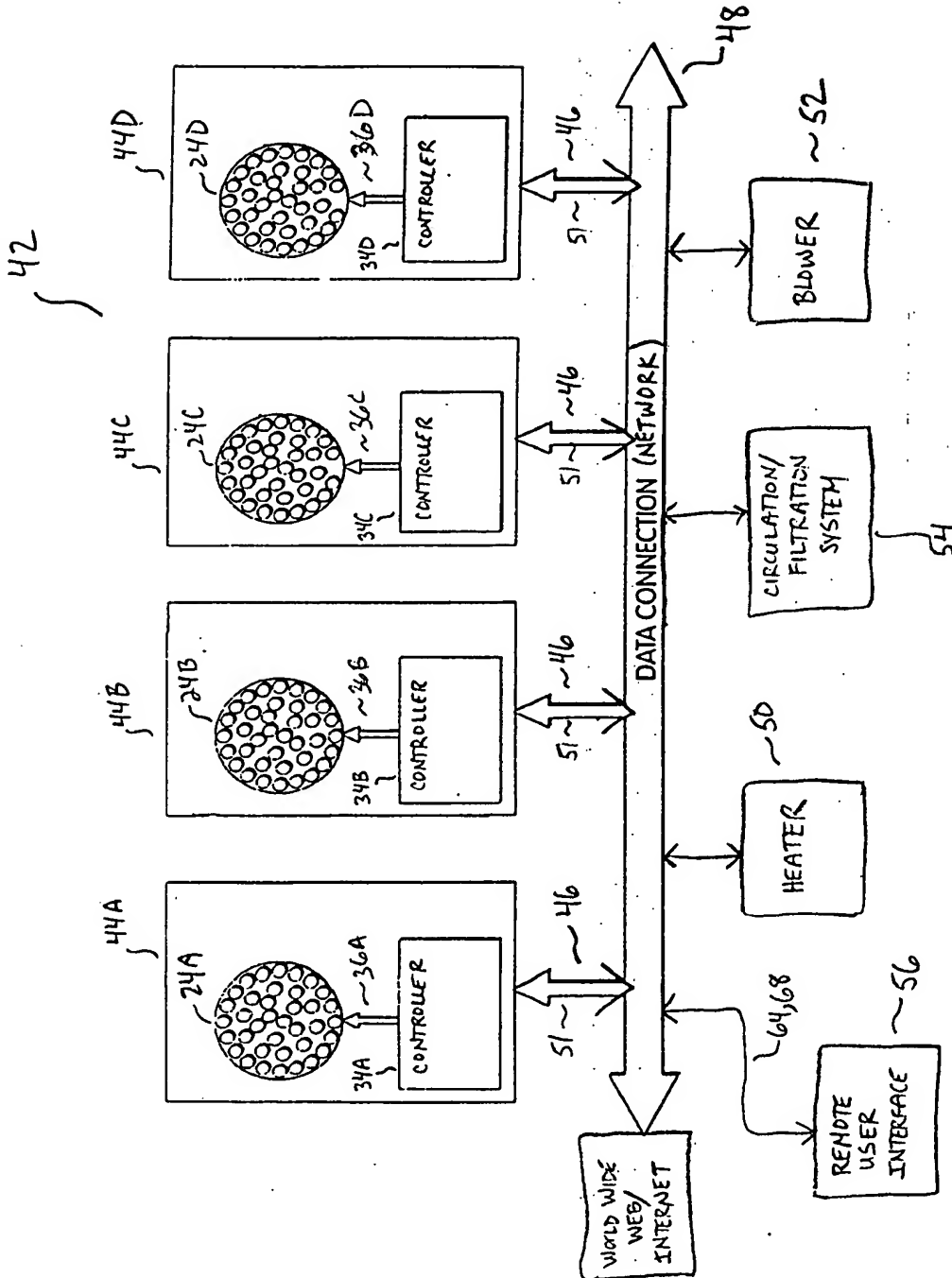


FIG. 4

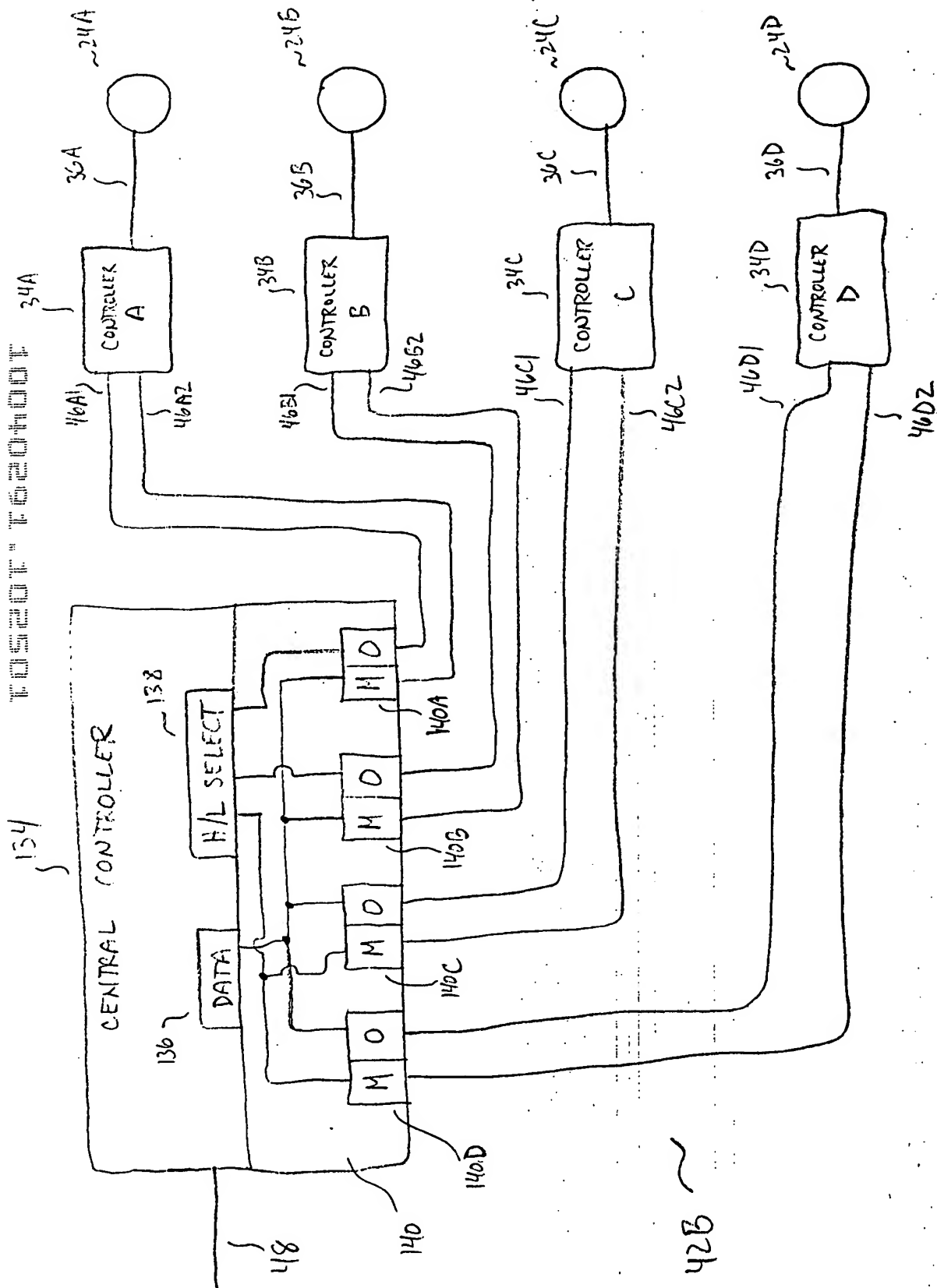


FIG. 4A

CONTROLLER	MODE (M)	OPTION (O)
A	DATA	LOGIC HIGH
B	DATA	LOGIC LOW
C	LOGIC HIGH	DATA
D	LOGIC LOW	DATA

FIG. 4B

10040291-102501

10040291.102501

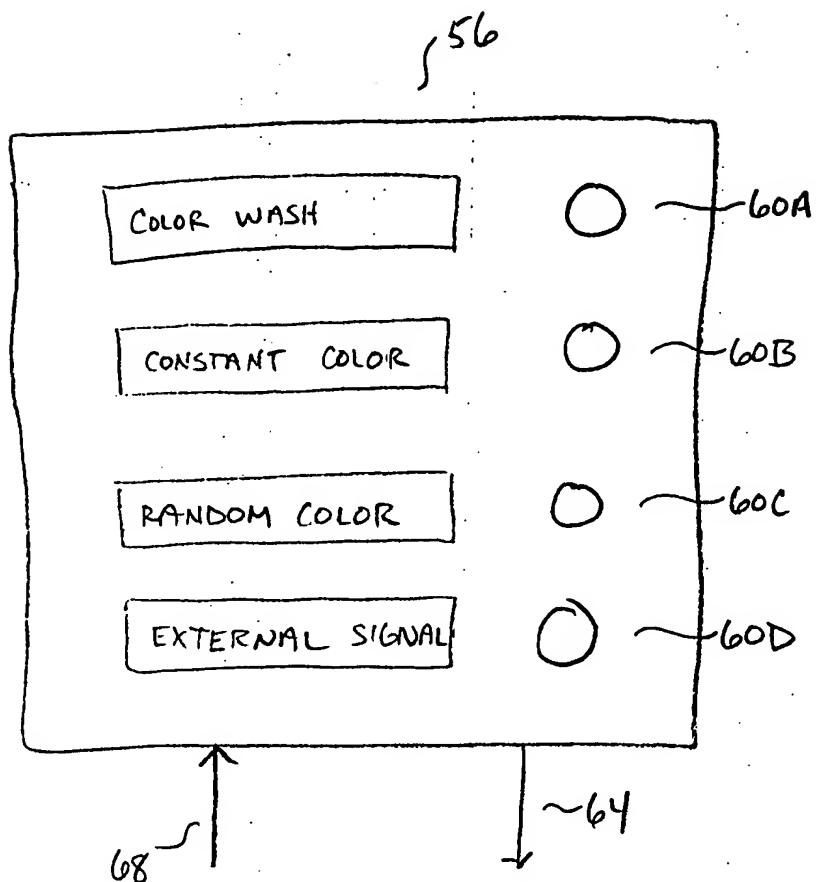


FIG. 5

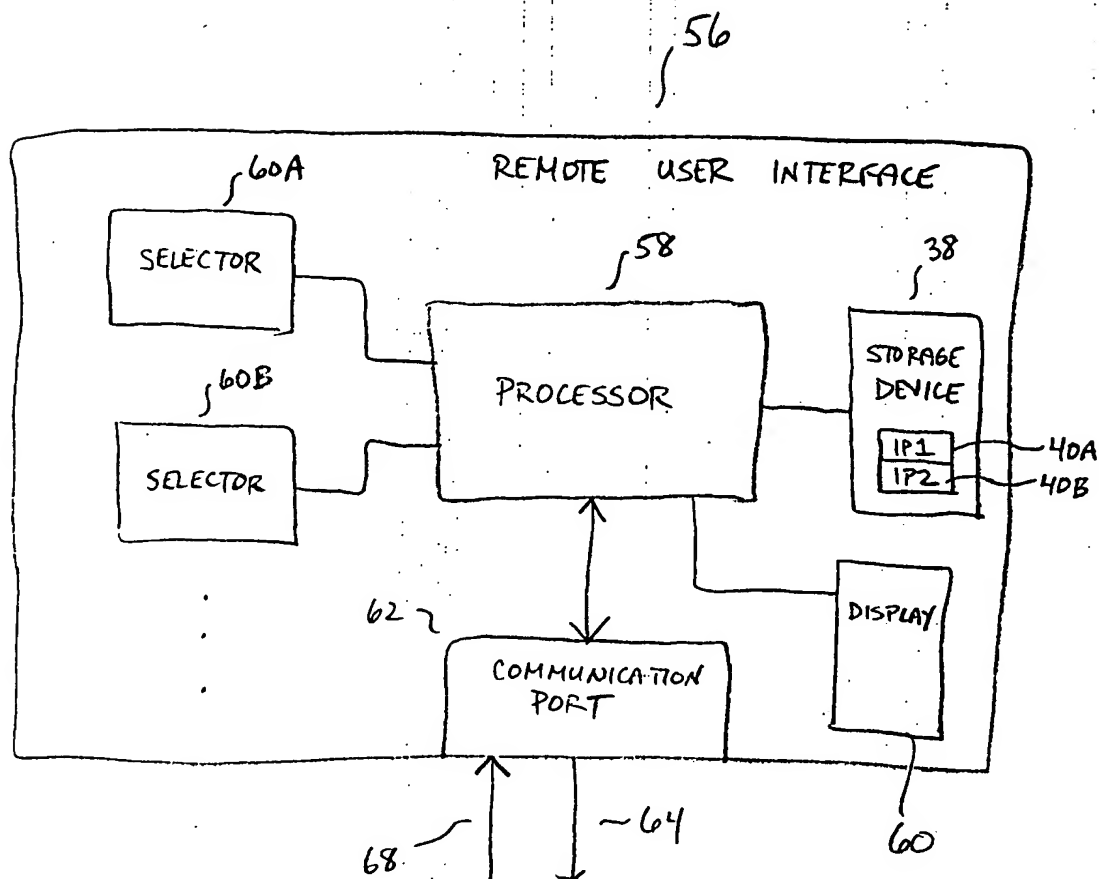


FIG. 6

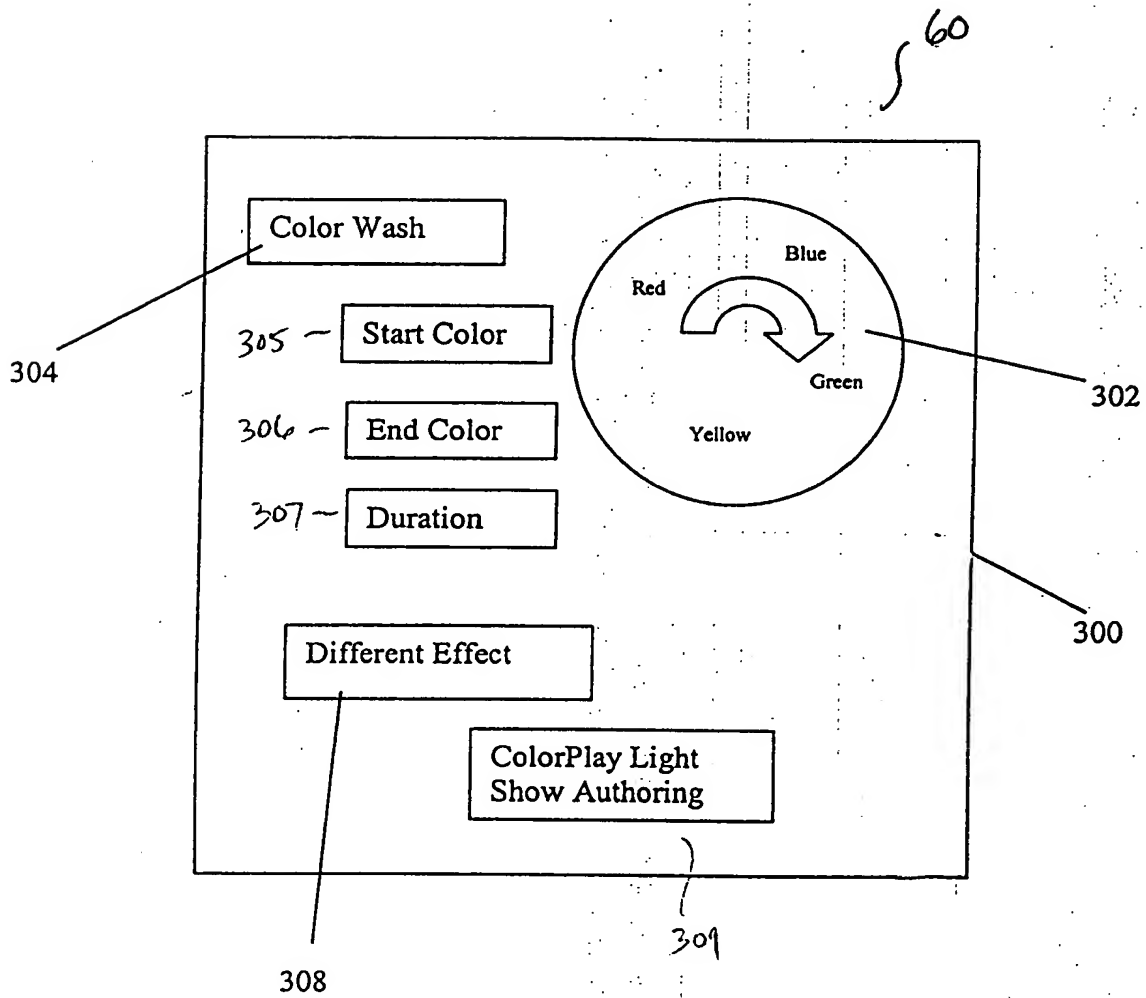


FIG. 7

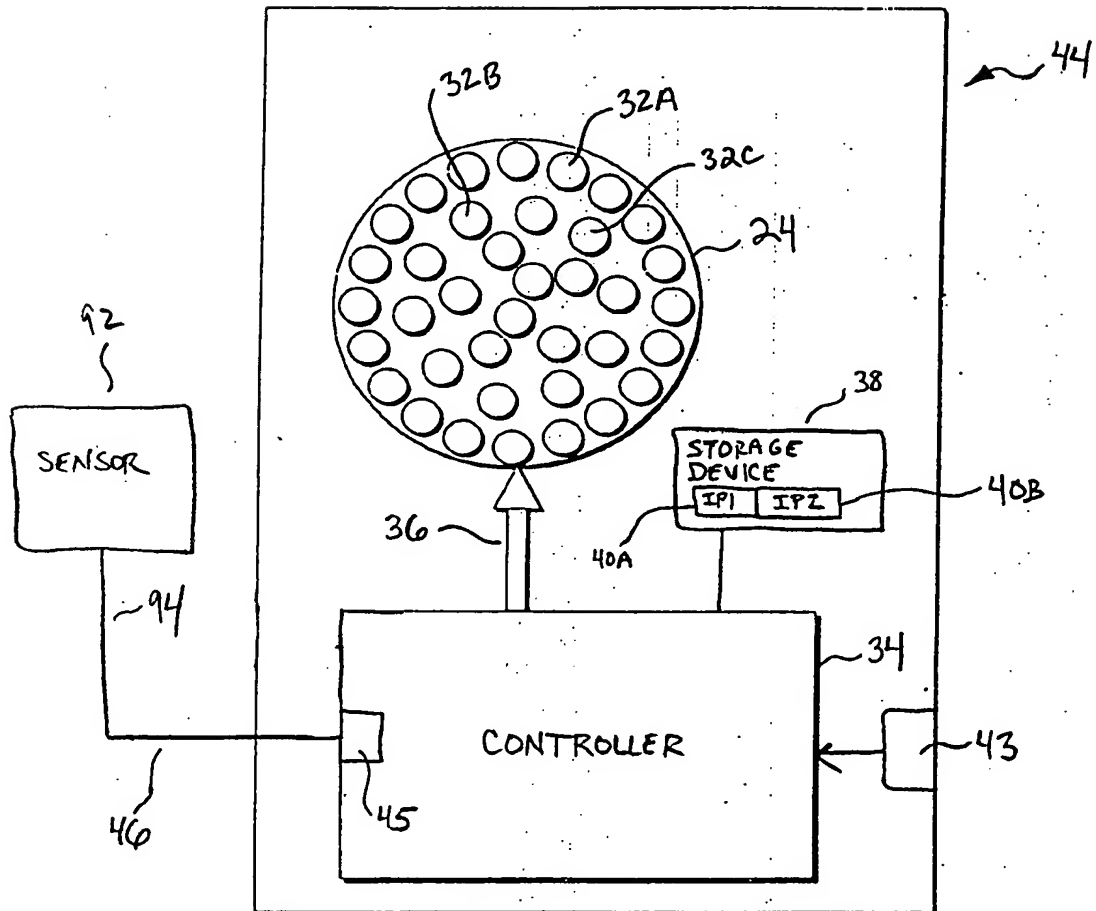
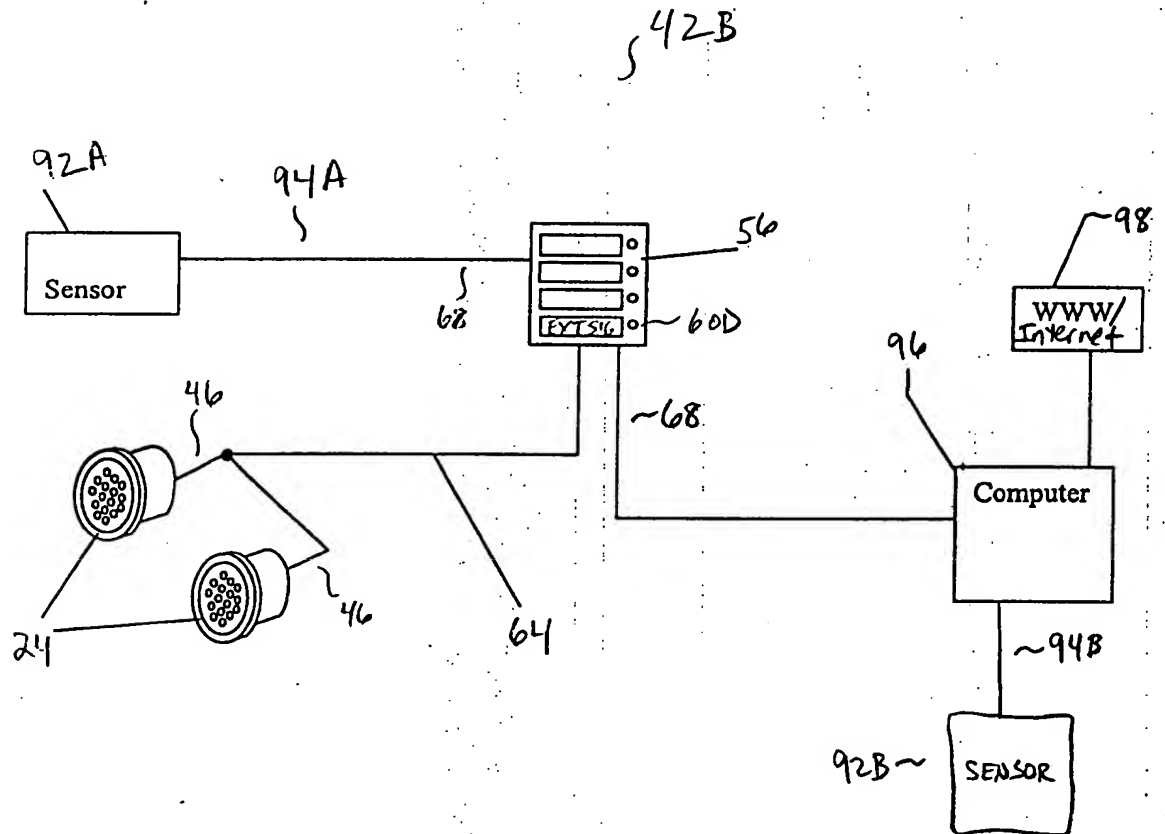


Fig. 8



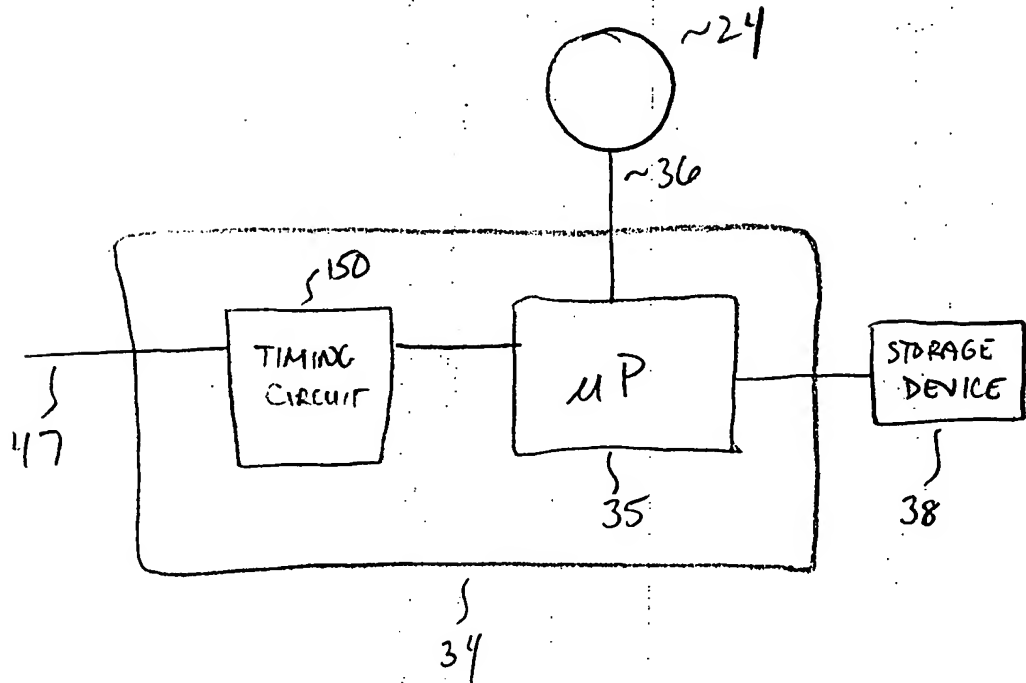
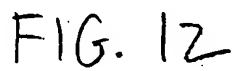


FIG. 10

10040291.102501





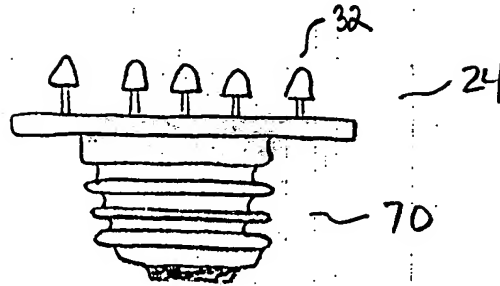


FIG. 13

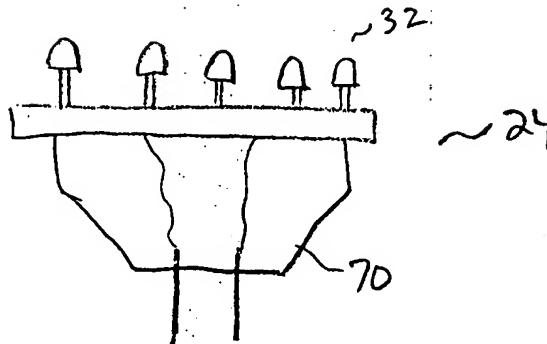
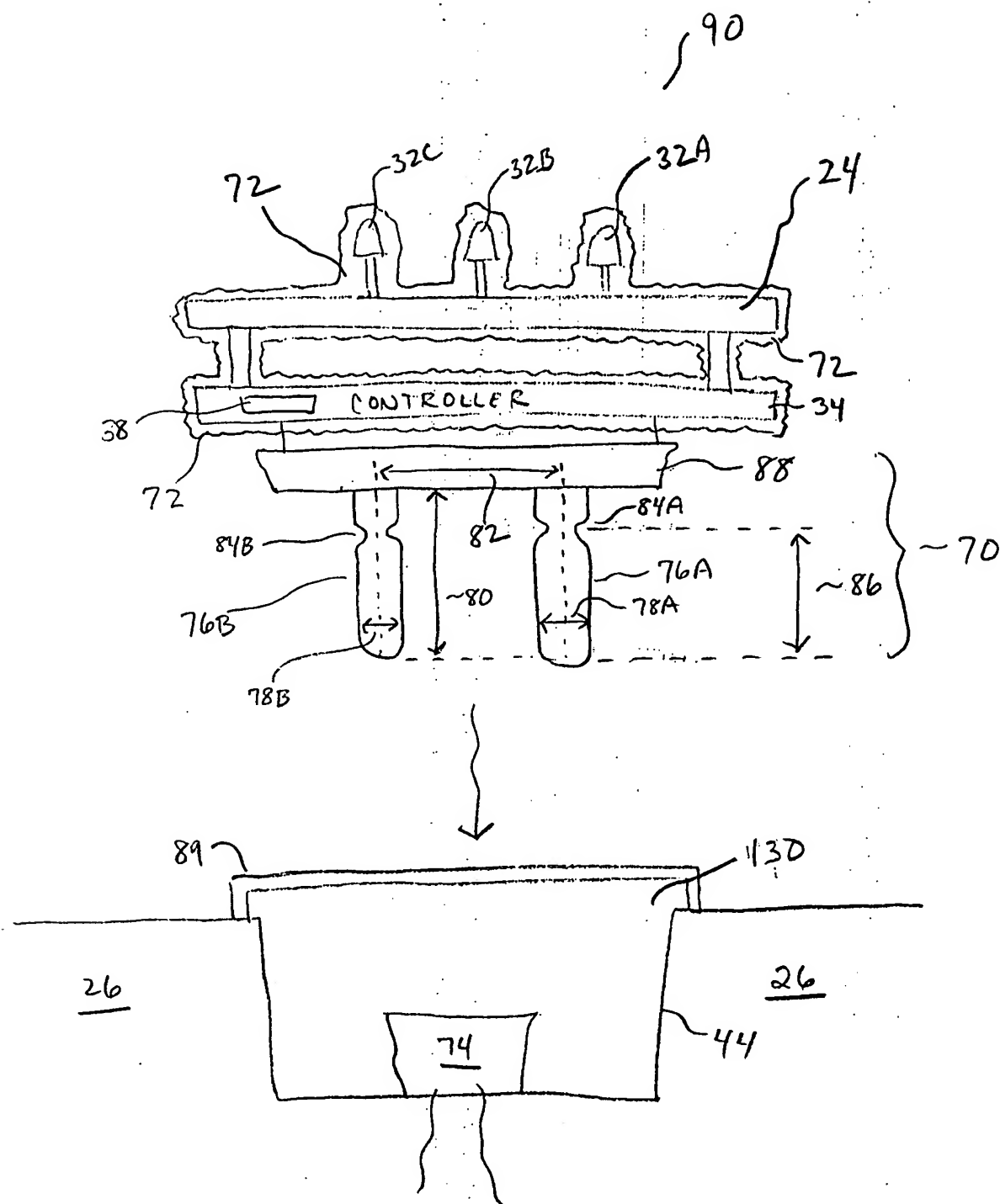


FIG. 14

10040291.10501

10040291.102501



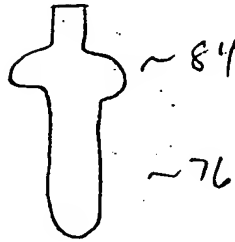


FIG. 16 A

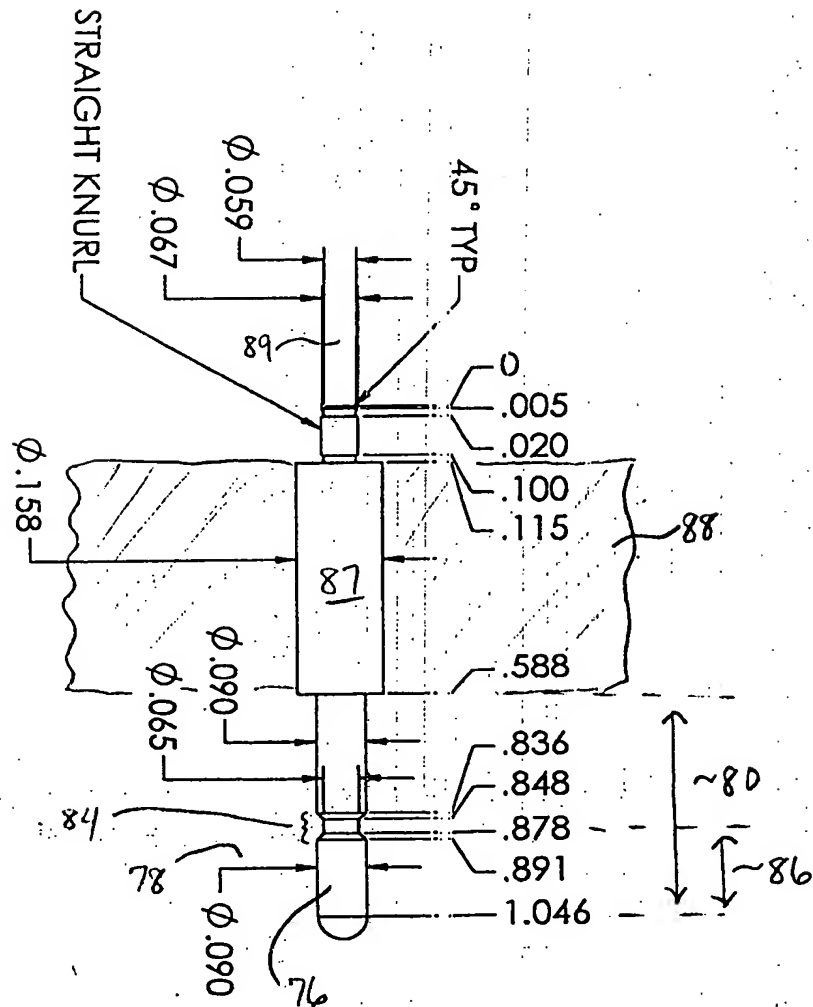


FIG. 16 B

10040291.102501

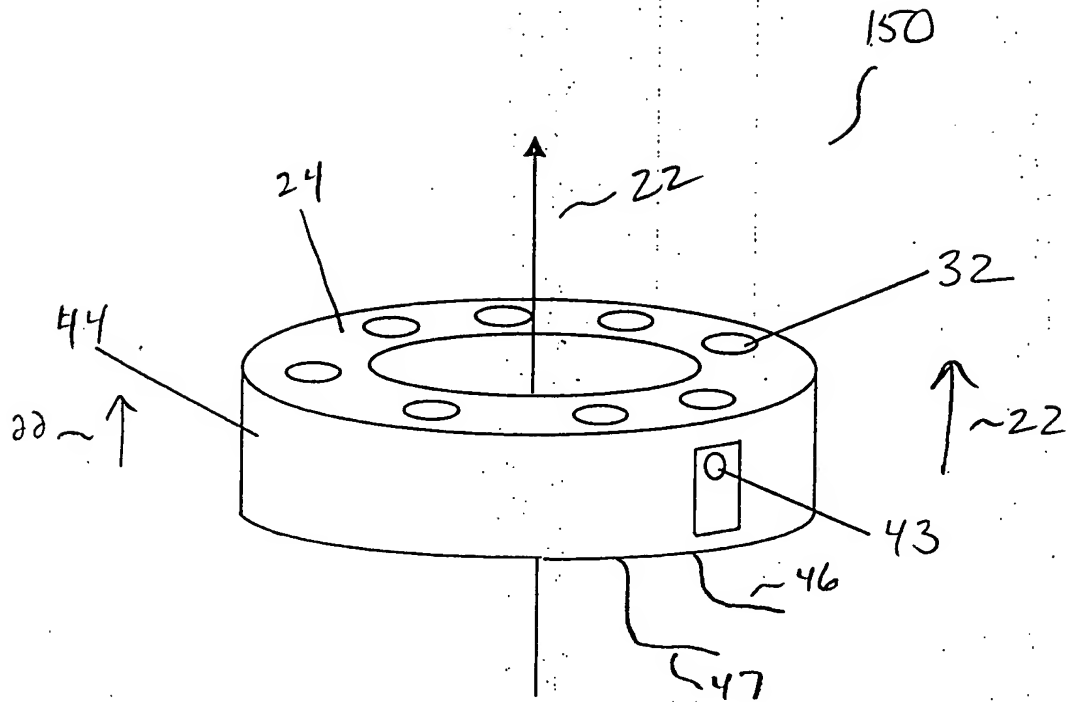


FIG. 17

10040291.102501

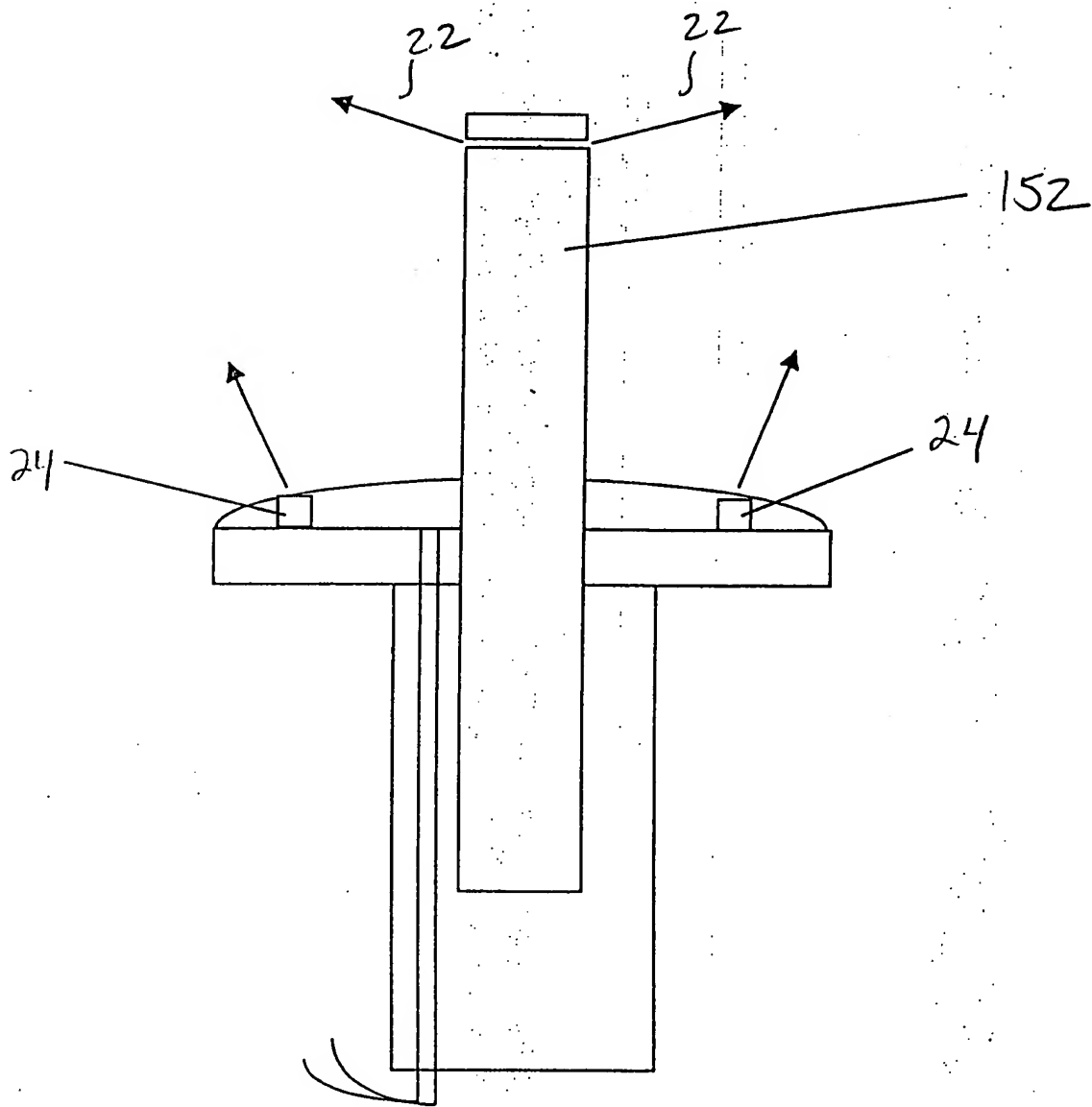


FIG. 18

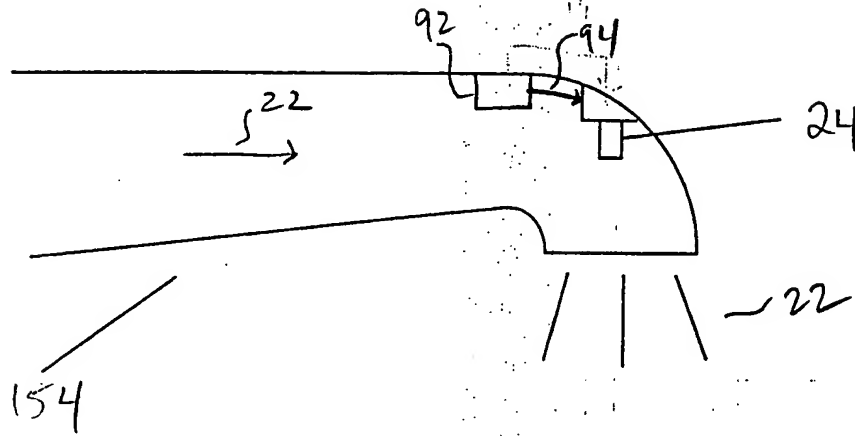


FIG. 19

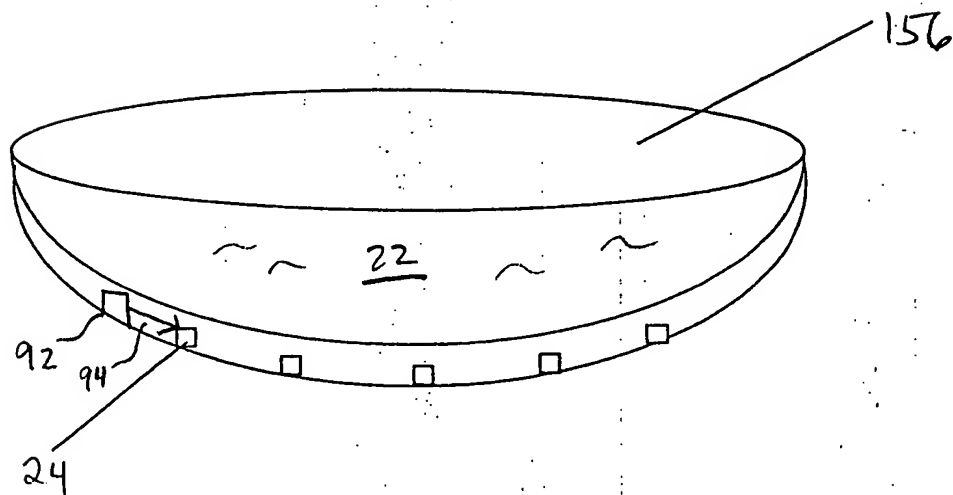


FIG. 20